Appl. No.: 10/002,461
Applicant(s): Keith R. Slavin
Filed: November 1, 2001
Title: Low Power, Hash-Content Addressable Memory Architecture
Art Unit: 2189
Examiner: Reba I. Elmore
Docket No.: PAT000955-000

COPY

DECLARATION UNDER RULE 132

I, Edward L. Pencoske, state as follows:

1.   I spoke with Keith R. Slavin by telephone on or around 6 December 2006 regarding his signing of a Rule 131 declaration.

2.   By an e-mail dated 6 December 2006, attached hereto as Exhibit A, I sent a Rule 131 declaration to Mr. Slavin for his execution.

3.   On 2 January 2007, I received the Rule 131 declaration back from Mr. Slavin, which is attached hereto as Exhibit B. On the last page of Exhibit B, Mr. Slavin wrote a note indicating that he would not sign the declaration.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true and, further, that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of this application or any patent issuing thereon.

_____ 12 Feb 07
Edward L. Pencoske   Date

Edward L Pencoske/JonesDay Extension 49531 12/06/2006 02:42 PM	To kslavin@dsl-only.net cc dvrangel@micron.com - bcc Subject hash cam disclosure

Thanks for returning my call. I have changed law firms. I am now with a much bigger firm and things are going very well. I hope your new job is working out well for you.

The reason I called you is that after the case was allowed, the patent examiner cited a new reference against your case. The new reference has an effective date that is before the filing date of your application, but later than the date of your invention disclosure. Under such circumstances, it is possible to "get behind" the reference by submitting a declaration stating that you conceived of the invention before the effective date of the reference, which is clearly the case here because your invention disclosure is dated before the date of the reference.

I have attached a declaration that we would like to submit to demonstrate that you conceived of the invention claimed in the application before the effective date of the reference. If the declaration looks ok to you, I would appreciate if you would sign and return the declaration to me. also attached is a pdf which contains the exhibits mentioned in the declaration. After the attachments is a link to you published application.

PII_1149367_1_PAT000955-000 Section 131 Declaration for Keith Slavin.DOC

exhibits.pdf

http://appft1.uspto.gov/netacgi/nph-Parser?Sect1=PTO1&Sect2=HITOFF&d=PG01&p=1&u=%2Fnetahtml%2FPTO%2Fsrchnum.html&r=1&f=G&l=50&s1=%2220030093616%22.PGNR.&OS=DN/20030093616&RS=DN/20030093616

Your application is publication no. 20030093616 in case the link fails

If you have any questions, feel free to call or e-mail

Thanks for the help - Best regards

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Filed: November 1, 2001
Title: LOW POWER, HASH-CONTENT ADDRESSABLE MEMORY ARCHITECTURE
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Examiner: Reba I. Elmore

Docket No.: DB000955-000

DECLARATION UNDER RULE 131

I, Keith R. Slavin, state as follows:

1.   I am the named inventor on U.S. application serial no. 10/002,461 ("the '461 application").

2.   In connection with the '461 application, I prepared and submitted an Invention Disclosure form to the Micron patent department. A copy of the first and last pages of the Invention Disclosure form are attached as Exhibits A and B, respectively.

3.   Attached to the Invention Disclosure form were four figures, attached hereto as Exhibit C. The figures of Exhibit C were read and understood by a witness, who signed and dated the figures on June 27, 2001.

4.   I have reviewed and am familiar with the '461 application.

- The Figure on Sheet 1 of Exhibit C shows the same subject matter as Figure 1 of the '461 application.

- The Figure on Sheet 2 of Exhibit C shows the same subject matter as Figure 2 of the '461 application.

- The Figures on Sheets 3 and 4 of Exhibit C show the same subject matter as Figure 3 of the '461 application.

5.   Independent claim 1 of the '461 application recites:

inputting an input word to a plurality of hash circuits, each hash circuit
being responsive to a different portion of said input word;
       outputting a hash signal from each hash circuit;
       enabling portions of a CAM in response to said hash signals;
       inputting said input word to said CAM;
       comparing said input word in the enabled portions of said CAM; and
       outputting information responsive to said comparing.

6.   Claim 1 reads on, for example, the Figure on Sheet 1 of Exhibit C and Figure 1 of the
'461 application.

7   Independent claim 8 recites:

hashing a comparand word;
precharging certain portions of a CAM in response to said hashing; and
inputting said comparand word to said CAM.

8.   Claim 8 reads on, for example, the Figure on Sheet 1 of Exhibit C and Figure 1 of the
'461 application.

9.   Independent claims 15 and 22 are method claims similar to claims 1 and 8, respectively.
Claims 15 and 22 recite many of the same limitations of claims 1 and 8 respectively, utilizing the same
physical elements outlined in those claims.

10.   Independent claim 28 of the '461 application recites:

a CAM for receiving a comparand word;
       a plurality of hash circuits connected in parallel, each for producing a
hash signal in response to a portion of the comparand word; and
       a circuit, responsive to said hash signals, for precharging portions of
said CAM.

11.   Claim 28 reads on, for example, the Figure on Sheet 1 of Exhibit C and Figure 1 of the
'461 application.

12.   Claim 34 of the '461 application recites:

a CAM;

a plurality of hash circuits each for producing a hash signal in response to a portion of a comparand word;

a plurality of memory devices responsive to said hash circuits;

enable logic, responsive to said plurality of memory devices, for enabling portions of said CAM; and

a delay circuit for inputting the comparand word to said CAM.

13. The CAM, the plurality of hash circuits, the enable logic, and the delay circuit read on, for example, the Figure on Sheet 1 of Exhibit C and Figure 1 of the '461 application.

14. Independent claim 41 is a method claim reciting a method for initializing the hardware shown in the Figure on Sheet 1 of Exhibit C and Figure 1 of the '461 application.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of this application or any patent issuing thereon.

_____   _____
Keith R. Slavin   Date

I no longer work for Micron, and am unwilling to sign any such statement because my memory of 4 years ago is not reliable, and I'm not willing to be put in a position for any type of punishment. I will say that I have always -3- acted with integrity.

[KEITH R. SLAVIN]   Keith Slavin, 29th Dec. 2006